



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Atty. Docket No.: 003921.00191

Charles PFEIL et al.

Serial No.: 10/780,902

Group Art Unit: TBA

Filed: February 19, 2004

Examiner: TBA

For: Parallel Electronic Design Automation: Shared Simultaneous Editing

INFORMATION DISCLOSURE STATEMENT

U.S. Patent and Trademark Office
220 20th Street S.
Customer Window
Crystal Plaza Two, Lobby, Room 1B03
Arlington, VA 22202

Sir:

Pursuant to the duty of good faith and candor as set forth in 37 C.F.R. §§ 1.56(a), 1.97 and 1.98 *et seq.*, Applicants submit herewith the attached Form PTO/SB/08A. Attached is a copy of reference number 8 on said SB/08A. The remaining references identified on the attached SB/08A were either provided in (and can be found in the file for) parent application 10/269,614 (now U.S. Patent 6,708,313), or are U.S. patent publications. Applicants respectfully request that the Examiner consider and enter all the documents cited on the enclosed Form PTO/SB/08A into the file of the above-identified application. Applicants also request an indication of the same by return of the Form PTO/SB/08A being initialed and dated by the Examiner.

Applicants further draw the Examiner's attention to the following copending applications (copies of which are also attached):

"Distributed Autorouting of Conductive Paths," filed June 18, 2004 (Attorney Docket No. 003921.00184)

"Protection Boundaries in a Parallel Printed Circuit Board Design Environment," filed June 18, 2004 (Attorney Docket No. 003921.00186)

"Reservation of Design Elements in a Parallel Printed Circuit Board Design Environment," filed June 18, 2004 (Attorney Docket No. 003921.00187)

No fees are believed due to ensure consideration of the attached documents by the Examiner. However, if any fees are required or an overpayment of fees made, the Commissioner is hereby authorized to debit or credit our Deposit Account No. 19-0733, as necessary.

Respectfully submitted,

Date: June 24, 2004

By:


H. Wayne Porter
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Attachments:

Form PTO/SB/08A

Reference no. 8 on Form PTO/SB/08A

"Distributed Autorouting of Conductive Paths," filed June 18, 2004 (Attorney Docket No. 003921.00184)

"Protection Boundaries in a Parallel Printed Circuit Board Design Environment," filed June 18, 2004 (Attorney Docket No. 003921.00186)

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Substitute for form 1449A/PTO

~~INFORMATION DISCLOSURE~~ STATEMENT BY APPLICANT

(use as many sheets as necessary)

Sheet	1	of	1
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Complete if Known

Application Number	10/780,902
Filing Date	February 19, 2004
First Named Inventor	Charles PFEIL et al.
Group Art Unit	TBA
Examiner Name	TBA
Attorney Docket Number	003921.00191

U.S. PATENT DOCUMENTS

[illegible]

FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³ - Number ⁴ - Kind Code ⁵ (if known)				

OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials *	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	8.	Michael K.W. Wu et al., "Development of an Integrated CAD Tool for Switching Power Supply Design with EMC Performance Evaluation," <i>IEEE Transactions on Industry Applications</i> , Vol. 34, No. 2 pp. pp. 364-373, Mar.-Apr. 1998)	
	9.	Geppert L. <u>IC Design on the World Wide Web</u> , IEEE Spectrum, October 2001, Pages 31-36.	

Examiner Signature		Date Considered	
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Applicant's unique citation designation number (optional). ² See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04.

³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶ Applicant is to place a check mark here if English language Translation is attached.

Burden Hour Statement: This form is estimated to take 2.0 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, P.O. Box 1460, Alexandria, VA 22313-1450. **DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO:** Assistant Commissioner for Patents, P.O. Box 1460, Alexandria, VA 22313-1450.